REMARKS

This amendment is submitted in response to an Office Action mailed July 14, 2004. Applicant respectfully requests reconsideration of the subject application as amended herein.

Claims 1-23 remain in the present application. Claims 3-12 were objected to as being dependent upon a rejected base claim, but were otherwise allowable. Claims 15-23 were allowed.

Applicant has amended claims 3-12 to place them in condition for allowance by including all of the elements of the base claim as suggested by the Examiner.

In the July 14, 2004 Office Action, claims 1, 2, and 14 were rejected under 35 U.S.C. § 103 as being unpatentable over U.S. Patent No. 5,919,262 issued to Kikinis et al. (hereinafter "Kikinis ") in view of U.S. Patent No. 6,229,292 issued to Redl et al. (hereinafter "Redl") and U.S. Patent No. 6,697,952 issued to King (hereinafter "King "). As discussed below, Applicant has amended the claims to clearly distinguish over the cited references.

For example, amended claim 1 includes:

A computer system comprising:
  a controller to generate a power state status signal to
indicate a power state of each of a plurality of components of the
computer system; and
  a voltage regulator to supply a first voltage level for the
plurality of components and to increase to a second voltage level
for the plurality of components when a first component of the

- 9 -

Atty. Docket No.: P12726
Application No.: 09/940,282

plurality of components enters a sleep state, as indicated by the power state status signal corresponding to the first component, and to further increase to a third voltage level for the plurality of components when a second component of the plurality of components enters a sleep state, as indicated by the power state status signal corresponding to the second component.

In amended claim 1, a voltage regulator supplies a voltage to a number of components and increases the voltage from level to level as more of the components enter a sleep state.

Applicant respectfully submits that none of the cited references suggest or disclose a voltage regulator that increases the voltage from level to level as more of the components enter a sleep state as claimed in amended claim 1.

Therefore, for at least the reasons stated above, Applicant respectfully submits that amended claim 1 is patentable over Kikinis in view of Redl and King.

Given that claims 2 and 14 depend from claim 1, Applicant respectfully submits that claims 2 and 14 are similarly patentable over Kikinis in view of Redl and King, for at least the reasons stated above.

In the July 14, 2004 Office Action, claim 13 was rejected under 35 U.S.C. § 103 as being unpatentable over Kikinis, Redl, and King, further in view of Intersil, "HIP6500B - Multiple Linear Power Controller with ACPI Control Interface", Data Sheet File Number 4870 (hereinafter "Intersil"). Claim 13 depends from claim 1, and Applicant respectfully submits that Intersil does not cure the deficiencies of Kikinis, Redl, and King discussed above with respect to

- 10 -

claim 1. Therefore, Applicant respectfully submits that claim 13 is likewise patentable over Kikinis in view of Redl, King, and Intersil, for at least the reasons discussed above.
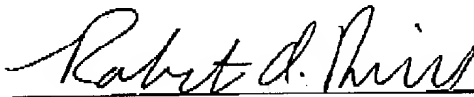
In conclusion, Applicant respectfully submits that claims 1-23 are now in a condition for allowance, and Applicant respectfully requests allowance of such claims.

Please charge any shortages and credit any overages to our Deposit Account No. 50-0221.

Respectfully submitted,

INTEL CORPORATION

Date: _8/12_, 2004

Robert A. Diehl
Reg. No. 40,992

INTEL LEGAL
SC4-202
P.O. Box 5326
Santa Clara, CA 95056-5326
Phone: (503) 712-1880
FAX: (503) 264-1729

- 11 -

Atty. Docket No.: P12726
Application No.: 09/940,282